Cascaded Multilevel Inverter and Its Application in STATCOM with REGS

Virendra Sharma[1], Lata Gidwani[2],
1 Ph.D. Research Scholar, Department of EE,Arya College of Engineering & IT , Jaipur, India.
2 Associate Professor, Department of EE, University College of Engineering, RTU kota , India.
Mail Id: virendrasharma_12@ yahoo.com

Abstract- Because of the broad application of multilevel converters in the high-power area, a cascaded multilevel voltage-source inverter with phase-shifted SPWM (PS-SPWM) switching scheme is proposed as a static Synchronous compensator (STATCOM). This can eliminate the bulky and weighty transformers and reduce power loss. In addition, the equivalent carrier frequency can be doubled and the output harmonics will be reduced compared with the STATCOM being put into operation. The operating principle and control methods are analysed in detail and the feasibility is validated by simulation with MATLAB.

Keywords: *Multilevel converter, Cascaded, PS-SPWM, STATCOM*

*****

1. Introduction

Since Nabea A. proposed the three-level inverter during the IAS annual conference in 1980, the multilevel converter, as a new breed of power converter option, has advanced rapidly and has been a hotspot for high-power applications. From previous research, it was shown that the multilevel converter has better performance than conventional converters in output harmonics spectra [1, 2]. There are three reported basic topologies of the multilevel converter: cascaded, Diode clamped, and capacitor clamped. Compared with the latter two, the cascaded multilevel converter has many distinct advantages:

- Switch devices required are less under the same Switching frequency and level number.
- The harmonic content is lower in the output voltage for a given switching frequency.
- Modularized circuit layout and packaging is possible because each cell has the same structure, and there are no extra clamping diodes as in the case of diode clamped topology, or voltage balancing capacitors as in the case of the capacitor clamped topology [3−7]. Due to the widespread use of high-power switch devices, a lot of reactive and harmonics current are produced, which have a worse effect on electric power equipment. Now, a high-order harmonic current and reactive current compensation are crucial tasks that need to be settled urgently in power systems.STATCOM is an advanced static var compensator introduced in 1990. It is different from the conventional var compensators such as thyristor-switched capacitors (TSC), thyristor-switched reactors (TCR) and the mechanically Switched capacitors. STATCOM is a static var compensator with no rotating parts, and is composed of new-generation high-power force-commutated semiconductor valve based inverters, DC capacitors and output transformers [8−13].

Nowadays, most of the STATCOMs that have been in use at home and abroad are made up of multi-pulse inverters and zig-zag transformers. The first STATCOM in China, which is manufactured by Tsinghua University and the Henan Electric Power Bureau, and put into use in March 1999, is also based on this configuration. The zig-zag transformers:

- Are the most expensive equipment in the system?
- Produce about 50 % of the total losses of the system.
- Occupy a large area of real estate (about 40 % of the total system).
- Cause difficulties in control due to saturation of the transformers.

Because of the advantages of cascaded multilevel inverters, this paper presents a STATCOM that adopts the cascaded multilevel inverter as the main topology to replace the multi-pulse inverter and bulky transformers used in the conventional STATCOM, and phase-shifted SPWM to replace the fundamental frequency modulation. The PS-SPWM principle, var compensation principle, and control method are analysed. A simulation system is created with MATLAB and the simulation results validate that the cascaded multilevel inverter with PS-SPWM is a better Choice for STATCOM.

2 System configuration of STATCOM

2.1 Cascaded inverter structure

Figure 1 shows the *Y*-configured 7-level cascaded inverter used in the STATCOM system. As shown in Fig. 1, one phase of the cascaded inverter consists of (7−1)/2 identical H-bridges, in which each bridge has its own separate DC source, and all the capacitors, switches and diodes have the same voltage and current ratings. The output voltage of each phase is summed by the output voltage of three H-bridge cells. This inverter can:

1) Generate sinusoidal waveform output voltage with the least harmonics.
2) Eliminate transformers used in conventional STATCOM.
3) make possible a direct connection to the distribution system without any additional transformers. A reactor is required between the system and the three-phase cascaded inverters, which serves as a current smoother to attenuate the high frequency current harmonics that the STATCOM generates

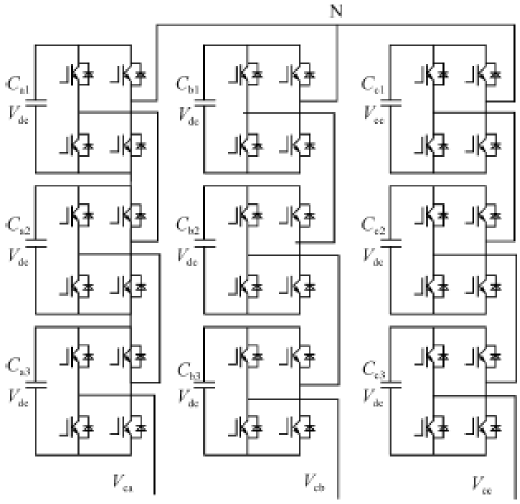


Fig. 1 the 7-level cascaded inverter used in the STATCOM system

2.2 PS-SPWM scheme

A so-called PS-SPWM switching scheme is proposed to operate the switches in the system. The scheme is briefly explained with the aid of Fig. 2 obtained by simulation with MATLAB. Three H-bridge inverters share the same modulating Sinusoidal signal u_a(t). The three triangular carrier Signals are for three H-bridge inverters, respectively. They are time shifted by θsh, so that the output of every H-bridge is time shifted, which leads to the equivalent switching frequency of the summed output voltage being increased, then the output harmonic content is reduced without increasing the switching frequency. Assuming the frequency ratio of the carrier and modulating sinusoidal signal is *k*c (*k*c= *f*/s *f* , *f*s, *f* are frequencies of carrier and modulating signal *ua*(*t*) respectively), the period of the triangular carrier is θc = c 2π *k* . The triangular carrier phase shifted for *n* modules cascaded inverters is θsh = c θ (2*n*) = c 2π (2*nk*), then the output voltage equivalent frequency ratio is *k*eff =2*nk*c. In the proposed STATCOM, *n* = 3, so the equivalent switching frequency of the output voltage is increased to 6*f*s.

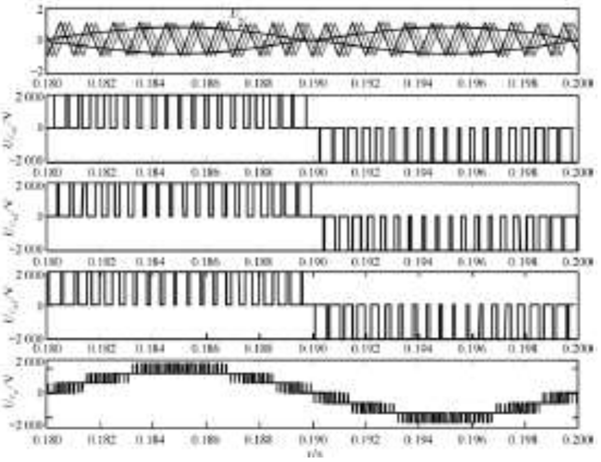


Fig2. Principle of PS-SPWM method

2.3 STATCOM system dynamic models and control of reactive power

The STATCOM based on cascaded multilevel inverters connects to the system through a current smoothing reactor as shown in Fig. 3.

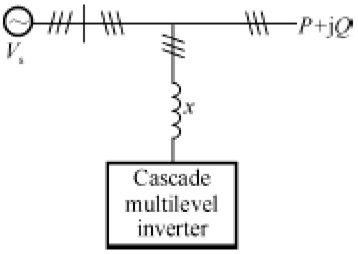


Fig. 3 Aconceptual STATCOM based on cascaded multilevel inverter

Figure 4 shows the single phase equivalent circuit of the STATCOM, where *V*s is the system voltage, V_c is the generated voltage of the STATCOM, *i* is the current drawn by the STATCOM, and *L* and *R* are the total AC inductance and resistance (including the smoother reactor and the impedance of the cascaded inverter). The exchange of the real Power and reactive power between the cascaded inverter and the power system can be controlled by adjusting the amplitude and phase of the inverter output voltage. In the case of an ideal inverter (the inverter need not absorb real power from the power system), the output voltage of the inverter is controlled to be in phase with that of the power system. To operate the STATCOM in capacitive mode, the magnitude of the inverter output voltage is greater than that of the power system, and the current *i* is leading. On the other hand, to operate the STATCOM in inductive mode, the magnitude of the inverter output voltage is controlled to be less than that of the power system, and the current *i* is lagging.

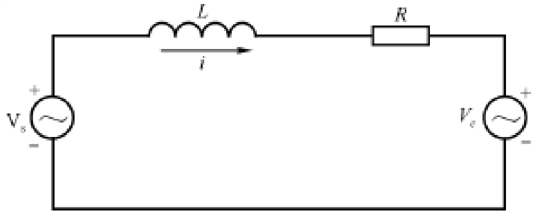


Fig. 4 Single phase equivalent circuit of the STATCOM system

However, in practice, the inverter is associated with internal losses caused by non-ideal power semiconductor devices and passive components. As a result, without any control, the capacitor voltage will decrease. To regulate the capacitor voltage, a small phase shift *α* between the inverter voltage and the power system voltage is introduced. Figure 5 illustrates the phasor diagram of voltage at a point of common coupling (PCC). The current projection on the power system voltage is *id*, and the vertical one is *iq*. The phase and magnitude of the current *i* is changed by changing the phase shift *α* or the amplitude of the inverter output voltage *Vc*, then the reactive exchange between the STATCOM and the power system is controlled

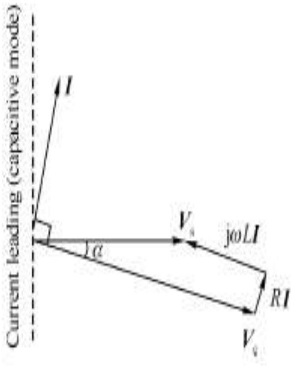


Fig. 5 Phasor diagram of voltage at PCC

From the equivalent circuit of Fig. 4, the model under *abc* coordinates can be obtained as Eq. (1):

$$L\frac{d}{dt}\begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} + R\begin{bmatrix} i_a \\ i_b \\ i_C \end{bmatrix} = \begin{bmatrix} V_{sa} \\ V_{sb} \\ V_{sc} \end{bmatrix} - \begin{bmatrix} V_{ca} \\ V_{cb} \\ V_{cc} \end{bmatrix}$$

where V_{sa}, V_{sb},V_{sc} are the three-phase voltages of the power system, V_{ca},V_{cb},V_{cc} are the three-phase inverter output voltages, and i_a, i_b, i_c are the three-phase inverter currents, whose referenced direction is shown in Fig. 4. Assuming that the power system voltage is ideal and sinusoidal, phase *A* voltage is $V_{sa}=V_m \sin \omega t$. The synchronous dq coordinates is shown in Fig. 6.

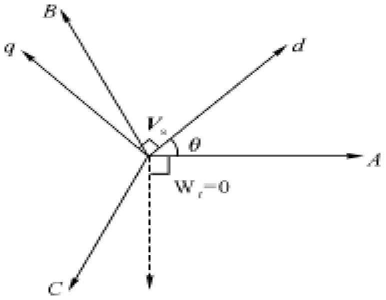


Fig. 6 The synchronous *dq* coordinates

If $\boldsymbol{T}_{abc\text{-}dq}$ is as follows:

$$\sqrt{\frac{2}{3}}\begin{bmatrix} \sin wt & \sin\left(wt-\frac{2\Pi}{3}\right) & \sin\left(wt-\frac{2\Pi}{3}\right) \\ \cos(wt) & \cos\left(wt+\frac{2\Pi}{3}\right) & \cos\left(wt+\frac{2\Pi}{3}\right) \end{bmatrix}$$

then one can get the *dq*-coordinate expressions using the synchronous reference frame transformation [$\boldsymbol{T}_{abc\text{-}dq}$].

$$L\frac{d}{dt}\begin{bmatrix} i_d \\ i_q \end{bmatrix} + R\begin{bmatrix} i_d \\ i_q \end{bmatrix} + wL\begin{pmatrix} -i_q \\ i_d \end{pmatrix} = \begin{bmatrix} V_{sd}-V_{cd} \\ V_{sq}-V_{cq} \end{bmatrix}$$

And

$$\begin{bmatrix} V_{sd} \\ V_{sq} \end{bmatrix} = \begin{bmatrix} \sqrt{\frac{3}{2}}V_m \\ 0 \end{bmatrix}$$

According to the instantaneous reactive power theory, the instantaneous active power P_C and the instantaneous reactive power Q_c can be obtained as:

$$P_C=\sqrt{\frac{3}{2}}V_m i_d$$

$$Q_c=\sqrt{\frac{3}{2}}V_m i_q$$

Therefore, the reactive power generated or absorbed by the inverter is directly controlled by adjusting *iq*. Likewise, the real power exchange can be controlled by adjusting *id*.

As a result, the reactive power and active power can be separately controlled. Then, *id* and *iq* are the active current component and reactive current component of the STATCOM: active power flows into the STATCOM when *id* is positive, and flows out when *id* is negative. The STATCOM generates leading reactive power when *iq* is positive and lagging reactive power when *iq* is negative.

3 Control scheme

As a STATCOM, it should have the characteristics of rapid dynamic response and small steady-state error. To obtain these performances, a feedback decoupling control is proposed, which is shown in Fig. 7. A PI controller is used for both active and reactive current control loops. The equivalent decoupling control diagrams for *id* and *iq* can be derived as shown in Fig. 8.

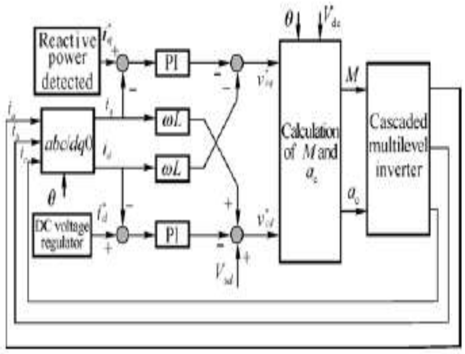


Fig. 7 Feedback decoupling control black diagram of the STATCOM

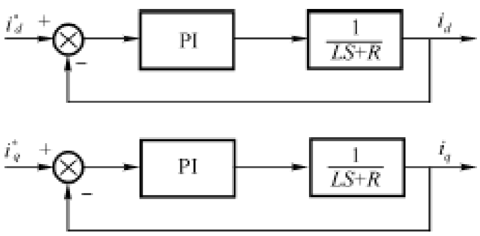


Fig. 8 Equivalent control diagram for *id* and *iq*

In the block diagram, *M* is the modulation index, and *α*c is the phase of the modulation signal. Their calculation equations are

$$M=\frac{\sqrt{{V_{cd}}^{2}+{V_{cq}}^{2}}}{nV_{dc}} \quad \alpha = \tan^{-1}\frac{V_{cq}}{V_{cd}}$$

where, n is the number of cascaded cells. Since the STATCOM output voltage is always needed to supply a small amount of active power to the STATCOM for component losses, the DC voltage regulator meets the demand of the active current. Interested readers can read Ref. [11] for a detailed description of this regulator

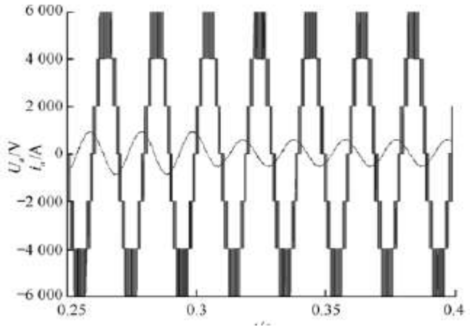
.

4 System simulation results

In order to validate the proposed inverter system, computer simulation using the MATLAB power system block set package is carried out with the main parameters: line-to-line

Voltage V_s = 6 000 V, f = 50 Hz, Q_{var} = ± 9 Mvar, f_s = 1 kHz, V_{dc} = 2 000 V, L = 3 mH.

The three-phase simulated system is based on the control scheme shown in Fig. 7. The STATCOM output phase voltage and current is shown in Fig. 9. It is obvious that the phase voltage is lagging behind the phase current by about $\pi/2$. The STATCOM is being operated in capacitive mode and compensating the inductive reactive power of the system



.

Fig. 9 STATCOM output phase voltage and phase current

Figure 10 is the frequency spectrum of the STATCOM output voltage *U*ca. It can be seen that the harmonics of the STATCOM output voltage only appear as side-bands centred around the frequency of 6*f*s(6 000 Hz) and its multiples. Therefore, the STATCOM output voltage has very high equivalent switching frequency, which simplifies the design and implementation of the filter

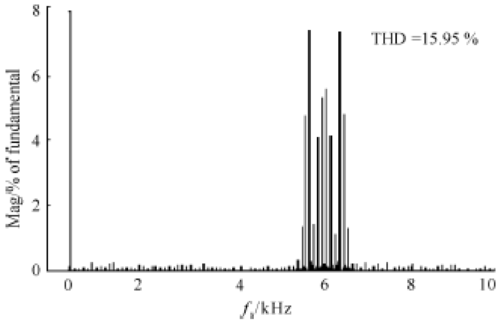


Fig. 10 Frequency spectrum of STATCOM output voltage

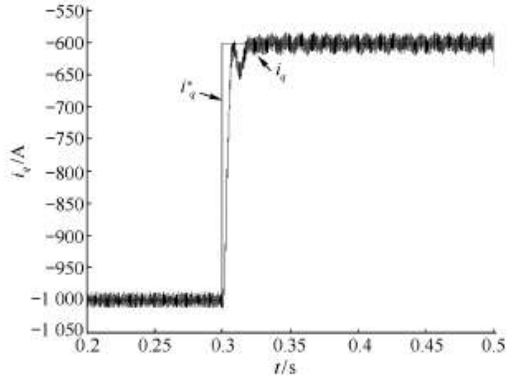


Fig. 11 Step response of the STATCOM

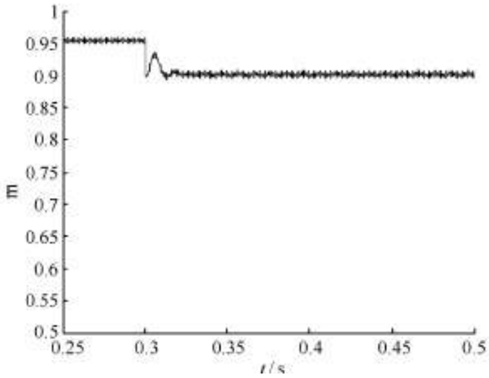


Fig. 12 The dynamic course of modulation index *M*

Figure 11 shows the simulation results of the dynamic response at a step-change reference of reactive current, which shows excellent dynamic response with a 20 ms time constant to the step change and no steady-error after stabilization.

The dynamic course of modulation index *M* during the step-change is shown in Fig. 12. *M* is adjusted to be smaller, and then the STATCOM can compensate for less inductive reactive power of the power system when it is operated in capacitive mode.

5 Conclusions

A cascaded multilevel inverter with phase-shift SPWM switching scheme is proposed for use as a STATCOM in this paper. The STATCOM topology offers several advantages over the other types of STATCOM such as reduced power loss, modular layout, the output changing linearly with the input, and so on. Furthermore, high equivalent switching is obtained, which can settle the conflict between devices switching frequency and devices capacity to a certain extent. These features make the cascaded multilevel inverter a better system for static synchronous compensation and this approach improves the power quality of renewable energy generation system.

References

[1] Zhou Jing-hua, Zhan Xiong, Su Yan-ming, The development of multi-module-cascade high-power inverter, Proceedings of the IEEE–IECON Conference, 2003: 2645−2649

[2] Xu Xiang-lian, Zou Yun-ping, Ding Kai, Liu Fei, Cascade multilevel inverter with phase-shift SPWM and its application in STATCOM, The 30th Annual Conference of IEEE Industrial Electronics Society, IEEE IECON’04, Busan: Korea, TC1-3, CD-ROM

[3] Wang Li-qiao, Wang Chang-yong, Huang Yu-yong et al., A cascade multi-level converter with phase-shifted SPWM technique, Hign Voltage Engineering, 2002，28(7): 17−19 (in Chinese)

[4] Fang Zheng-peng, Lai Jih-sheng, McKeever J. W., A multilevel voltage-source inverter with separate DC sources for static VAr generation, IEEE Transactions on Industry Applications, 1996, 32(5): 1130−1138

[5] Liang Yi-qiao, Nwankpa C. O., A new type of STATCOM based on cascading voltage-source inverters with phase-shifted unipolar SPWM, IEEE Transactions on Industry Application, 1999, 35(5): 1118−1123

[6] Fang Zheng-peng, Lai Jih-sheng, Dynamic performance and control of a static VAr generator using cascade multilevel inverters, IEEE Transactions on Industry Applications, 1997, 33(3): 748−755

[7] Al-Hadidi, H. K., Menzies R. W., Investigation of a cascade multilevel inverter as an STATCOM, Power Engineering Society General Meeting, 2003, IEEE, 2003, 1: 193

[8] Shen Fei, Wang Ya-lan, Liu Wen-hua et al., Analyses and com comparison of large capacity STATCOM circuit configuration, Automation of Electric Power Systems, 2003, 27(8): 59−66 (in Chinese)

[9] Lehn, P. W., Iravani M. R., Experimental evaluation of STATCOM closed loop dynamics, IEEE Transactions on Power Delivery, 1998, 13(4): 1378−1384

[10] Dong Shen, Lehn P. W., Modeling, analysis, and control of a current source inverter-based STATCOM, IEEE Transactions on Power Delivery, 2002, 17(1): 248−253

[11] Chen Yi-qiang, Mwinyiwiwa B., Wolanski Z. et al., Regulating and equalizing DC capacitance voltages in multilevel STATCOM, IEEE Transactions on Power Delivery, 1997, 12(2): 901−907

[12] Garica-Gonzalez P., Garcia-Cerrada A., Control system for a PWM-based STATCOM, IEEE Transactions on Power Delivery, 2000, 15(4): 1252−1257

[13] Lehn, P. W., Exact modeling of the voltage source converter, IEEE Transactions on Power Delivery, 2002, 17(1): 217−222