

Third harmonic distortion and Phase shift analysis of Improved Transformless Voltage Source Inverter For Photovoltaic Grid-Connected System with Common-Mode Leakage Current Elimination

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Abstract- This paper analysis the phase shift between output voltage and current in the grid connected transformerless inverter due to existence of the filter inductance. For the elimination of the common-mode leakage current in the transformerless photovoltaic grid-connected system, an improved single-phase inverter is designed by adding two additional switches and filter inductance due to which phase shift occurs at the grid. For eliminating common mode leakage current in the transformerless inverter by two strategy one is unipolar sinusoidal pulse width modulation (SPWM) control strategy and another Bipolar sinusoidal pulse width modulation (SPWM) control strategy. By applying both this method with different inductance value we analysis the optimize value so that we will generate a sine wave with fewer harmonics, low phase between grid voltage and grid current, less cost and a simpler design.

Keywords — Common-mode leakage current, junction capacitance, phase shift, photovoltaic (PV) system, unipolar sinusoidal pulse width modulation (SPWM) strategy, bipolar sinusoidal pulse width modulation (SPWM) strategy, transformerless inverter, filter inductance. Harmonics distortion.

I. INTRODUCTION

Nowadays, the world needs the electricity to be increased. The main reasons for the energy increase demand are the population, the economy growth and the rapid depletion of fossils based on energy reserve and rapid growth of energy demand. Then, it must research for an alternative source of power generation. One of these sources is a renewable energy which possibly has no harm on the environment.

Energy technology plays a very important role in economic and social development of any country. Presently maximum electrical energy demands are fulfilled by natural gas, coal, petroleum and hydro. To mitigate the negative environmental impact from the electricity supply industry, now there is increasing its efforts to promote renewable energy (RE) and energy efficiency (EE). Solar energy is the world's most abundant permanent source of energy that is also an important and environmentally compatible source of renewable energy.

In this context, lots of research needs to be done in order to achieve a reliable and efficient energy. So that at the grid-connected photovoltaic (PV) systems is introduced with many topology, Different types of photovoltaic cell will yield different energy output, meanwhile the controlling technique of inverter is very crucial in the PV system. Inverter design should consider the size and capacity of the plant, on the other hand choosing the right controlling technique is needed as well in order to achieve an efficient renewable energy system.

In a normal transformer, inverter PV grid system combination transformers consist of several stages so due to this system system complexity and reduces the system efficiency also making the whole system bulky and tough to install. Hence to reduce the size and increasing the efficiency the best way is to remove the isolation transformer. But due to this removing of transformer there is common mode leakage current is presence of parasitic capacitance between the PV panel and the ground. The common-mode leakage current flows via parasitic capacitance of the panel to the system which is not meant to be energized. It causes several personal problem like safety problems, degradation in panels, system losses, reduces the grid-connected current quality and induces the severe conducted and radiated electromagnetic interference.

In order to minimise the common-mode leakage current, the best method of controlling are either with unipolar sinusoidal pulse width modulation (SPWM) or bipolar sinusoidal pulse width modulation (SPWM). But due to all this method There is actually a phase shift between the output voltage U_{AB} and output current I_g in the grid-connected inverter due to the existence of the filter inductance.

II. PWM method

An inverter contains electronic switches, it is possible to control the output voltage as well as optimize the harmonics by performing multiple switching within the inverter with the constant dc input voltage V_d . The PWM principle to control the output voltage is explained in figure 1. The fundamental voltage

V_1 has the maximum amplitude ($4V_d / \pi$) at square wave, but by creating two notches as shown, the magnitude can be reduced. If the notch widths are increased, the fundamental voltage will be reduced. Circuit model of a single-phase inverter with a center-taped grounded DC bus, and Fig 1.1 illustrates principle of pulse width modulation.

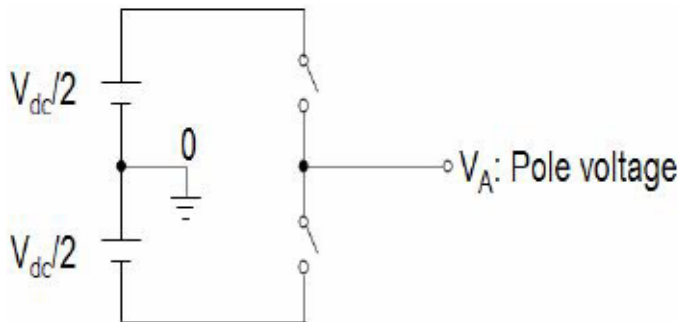


Fig.1.1.Circuit model of a single-phase inverter.

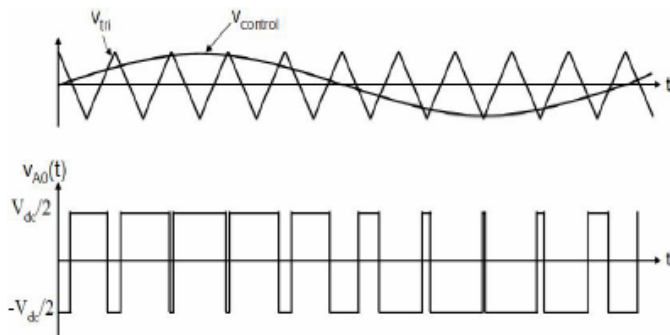


Fig.1.2. Pulse width modulation

The inverter output voltage is determined in the following:

- When $V_{control}$ is greater than V_{tri} , $V_{A0} = V_{dc}/2$
- When $V_{control}$ is less than V_{tri} , $V_{A0} = -V_{dc}/2$

The modulation method is very crucial part of control structure. As the following parameter can feature Low content of higher harmonics in voltage and current, Low frequency harmonics. etc.

The average value of voltage (and current) fed to the load is controlled by turning the switch

between supply and load on and off at a fast pace.

The basic advantage of PWM is that power loss in the switching devices is very low. When a switch is off there is practically no current, and when it is on, there is almost no voltage drop across the switch. Power loss, being the product of voltage and current, is thus in both cases close to zero.

III. Basic Spwm method:

The sinusoidal PWM technique is very popular for industrial converters. The basic principle of SPWM is that where an isosceles triangle carrier wave of frequency f_c is compared with

the fundamental frequency of sinusoidal modulating wave, and the points of intersection determine the switching points of power devices. This method is also known as the triangulation, sub harmonic, or sub oscillation method. The notch and pulse widths of V_{A0} wave vary in a sinusoidal manner so that the average or fundamental component frequency is the same f and its amplitude is proportional to the command modulating voltage. The same carrier wave can be used for all three phases, as shown. The typical wave shape of line and phase voltages for an isolated neutral load can be plotted graphically as shown in figure 1.3. The Fourier analysis of the V_{A0} wave is somewhat involved and can be shown to be of the following form:

$$V_{an} = 0.5mV_d \sin(\omega t + \Phi) + \text{high-frequency } (M\omega_c + N\omega) \text{ terms}$$

In spwm, the pulse width is a sinusoidal function of the angular position of the pulse in a cycle. The desired output voltage is achieved by comparing the desired reference waveform (modulating signal) with a high-frequency 'triangular carrier' wave as depicted schematically in Fig.1.3. Depending on whether the signal voltage is larger or smaller than the carrier waveform, either the positive or negative dc bus voltage is applied at the output.

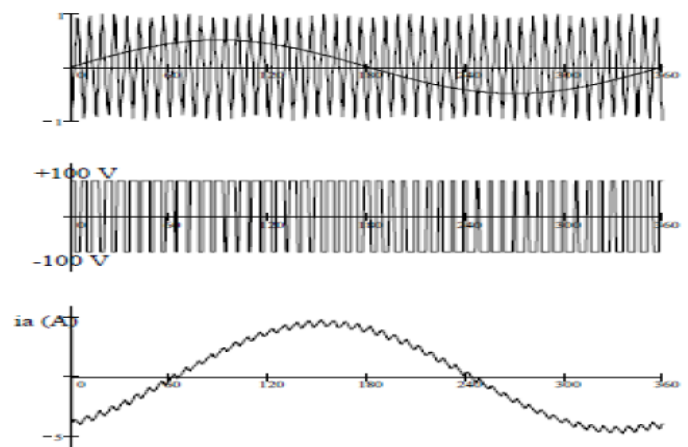
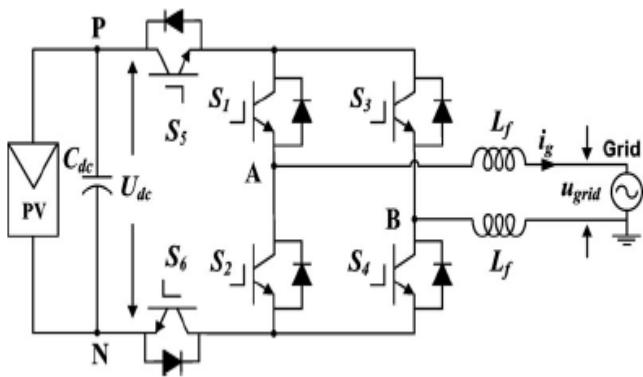


Fig 1.3 Sinusoidal PWM

Note that over the period of one triangle wave, the average voltage applied to the load is proportional to the amplitude of the signal (assumed constant) during this period. The resulting chopped square waveform contains a replica of the desired waveform in its low frequency components, with the higher frequency components being at frequencies of a close to the carrier frequency. However, a higher carrier frequency does result in a larger number of switching's per cycle and hence in an increased power loss. Typically switching frequencies in the 2-15 kHz range are considered adequate for power systems applications. Also in three-phase systems all three waveforms are symmetric.

IV. Improved inverter topology.



V. Phase Shift Between Output Voltage and Current

As filter inductance present in the circuit hence due to this phase shift between the output voltage U_{AB} and output current i_g in the grid-connected inverter.

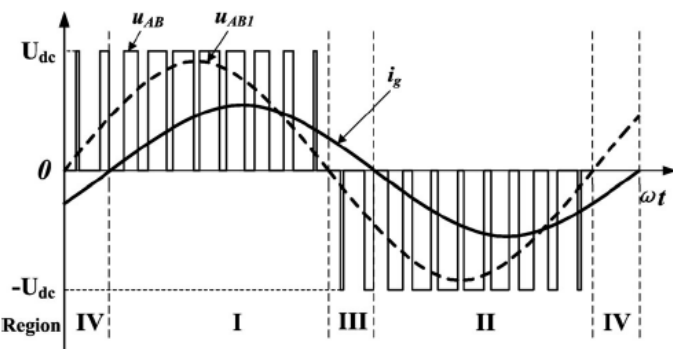
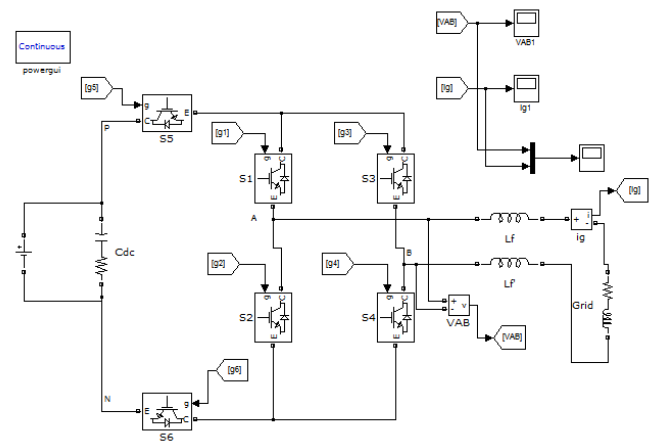


Fig. 1.4

Practical waveforms of the improved inverter considering phase shift.

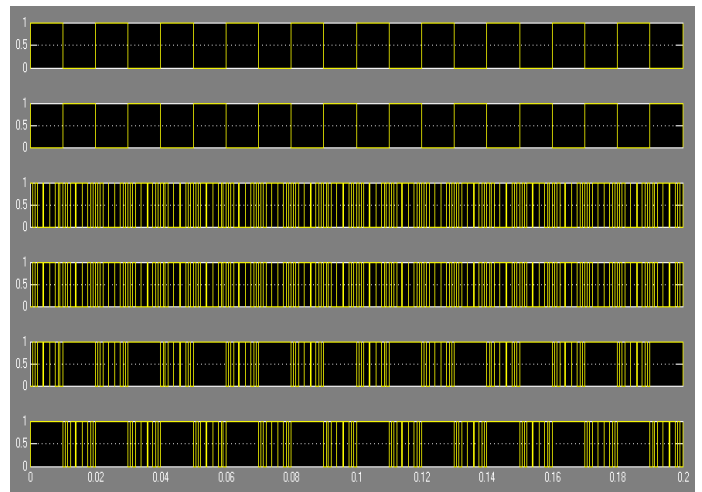
Fig. 1.4 shows the practical waveforms of the improved inverter when considering the phase shift, where U_{AB1} is the fundamental component of the output voltage U_{AB} . In Region I and Region II the output voltage and current are in the same direction. In Region III, the output current is positive. Meanwhile, the output voltage is negative and modulated according to the operation principle in the negative half cycle. Therefore, if the unipolar SPWM strategy continuously rotate to generate $-U_{dc}$ and zero states and the double-frequency SPWM strategy is used, continuously rotate to generate $-U_{dc}$ and zero states

VI. MATLAB / SIMULINK MODEL



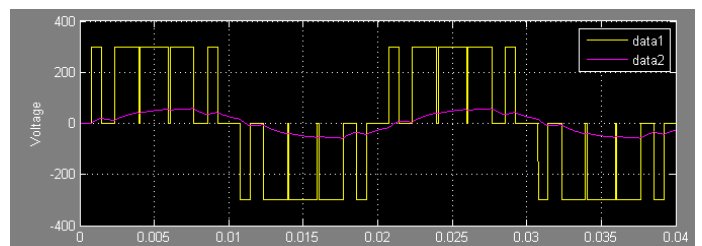
Improved inverter simulation model

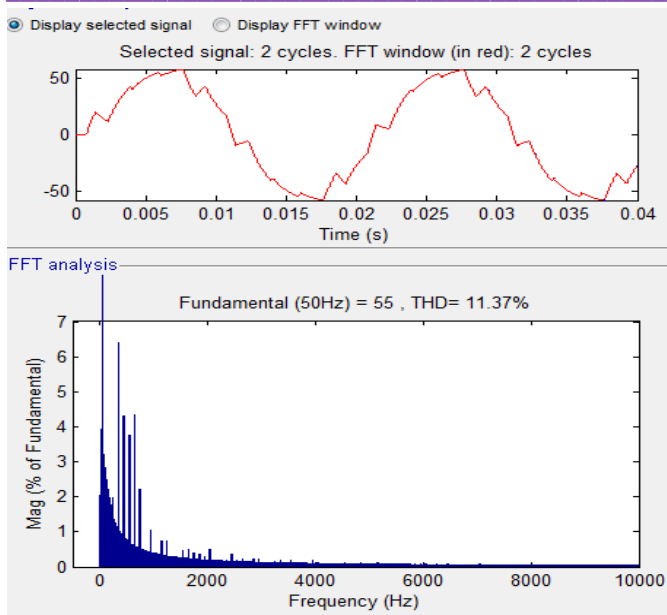
VII. Unipolar Spwm Control and results



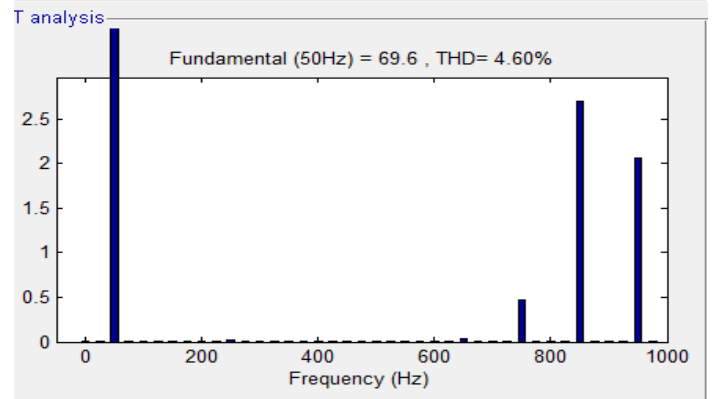
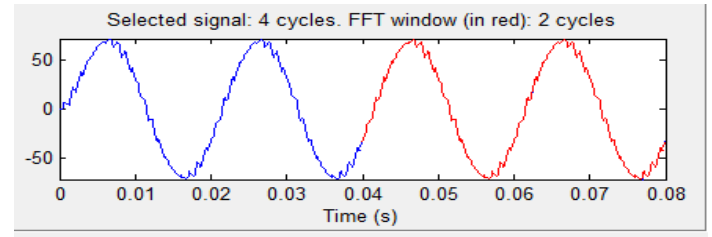
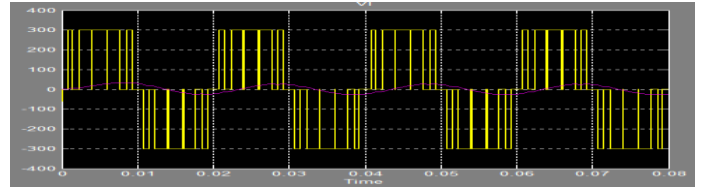
Switching technique waveform

For Filter Inductance $L = 4mH$

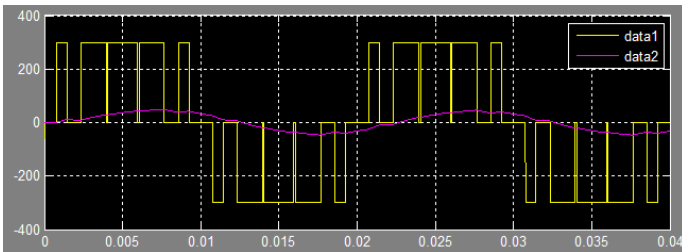




For Filter Inductance L = 4mH



For L= 8mH

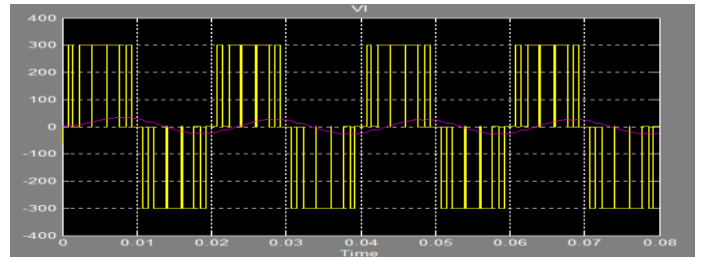


Table

Unipolar spwm inductance Vs THD of grid current

Inductance	THD
4mH	11.37
8mH	9.31
12mH,	6.27
16mH	6.14
20mH	6.02

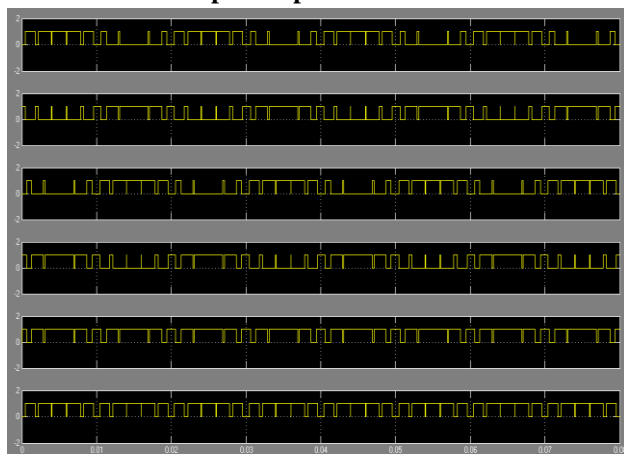
8mh



Bipolar spwm inductance Vs THD of grid current

Inductance	THD
4mH	4.60
8mH	2.95
12mH,	2.52
16mH	2.36
20mH	2.28

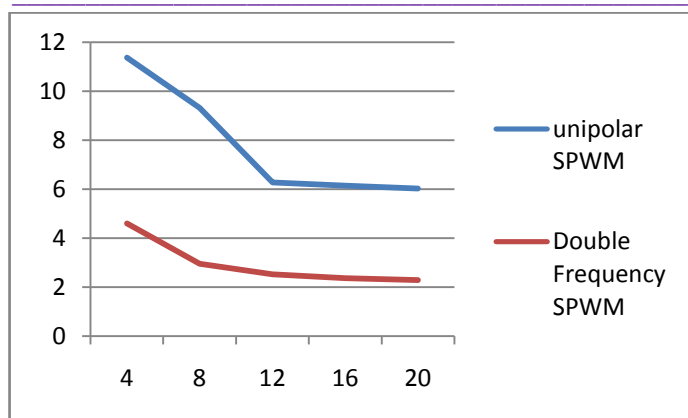
VIII. Bipolar Spwm Control and results



Switching technique waveform

IX. Comparison Between Unipolar and Bipolar SPWM Technique

Filter Inductance in mH	4	8	12	16	20
Unipolar SPWM	11.37	9.31	6.27	6.14	6.02
Double Frequency SPWM	4.6	2.95	2.52	2.36	2.28



X. Conclusion

This paper compares the Third harmonics value of improved transformerless inverter photovoltaic grid connected system with different inductance value of the filter and also observe the phase shifting of output voltage and output current that for grid. The unipolar SPWM control technique is applied with three-level output with different inductance value of filter is compared with the bipolar SPWM control technique. From the result we observe that the THD value is less in case of bipolar SPWM control technique but as switching is high in case of bipolar SPWM to that of unipolar SPWM. Hence efficiency of unipolar is high as switching losses are less at unipolar.

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